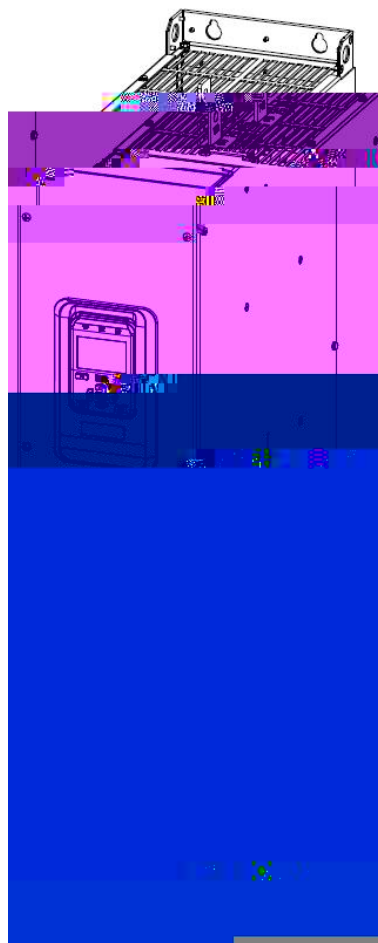


HF680N

DC-DC

V1.00

Wuhan Guide Technology Co.,Ltd.



DC-DC

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1.

1.1

1.2

1.3

2.

2.1

2.2

2.3

2.4 DC-DC

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3.2

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3.4

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4.1 DC-DC

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11.1

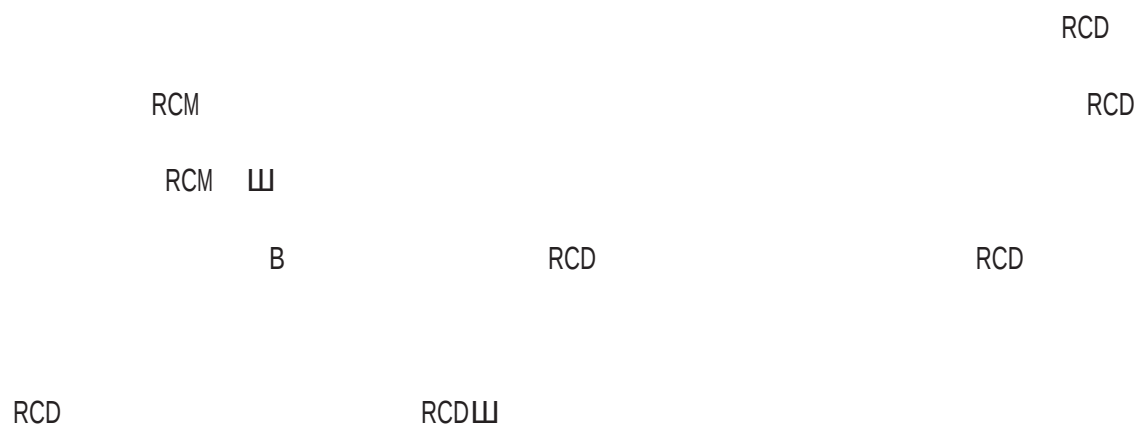
11.2

11.3

11.4

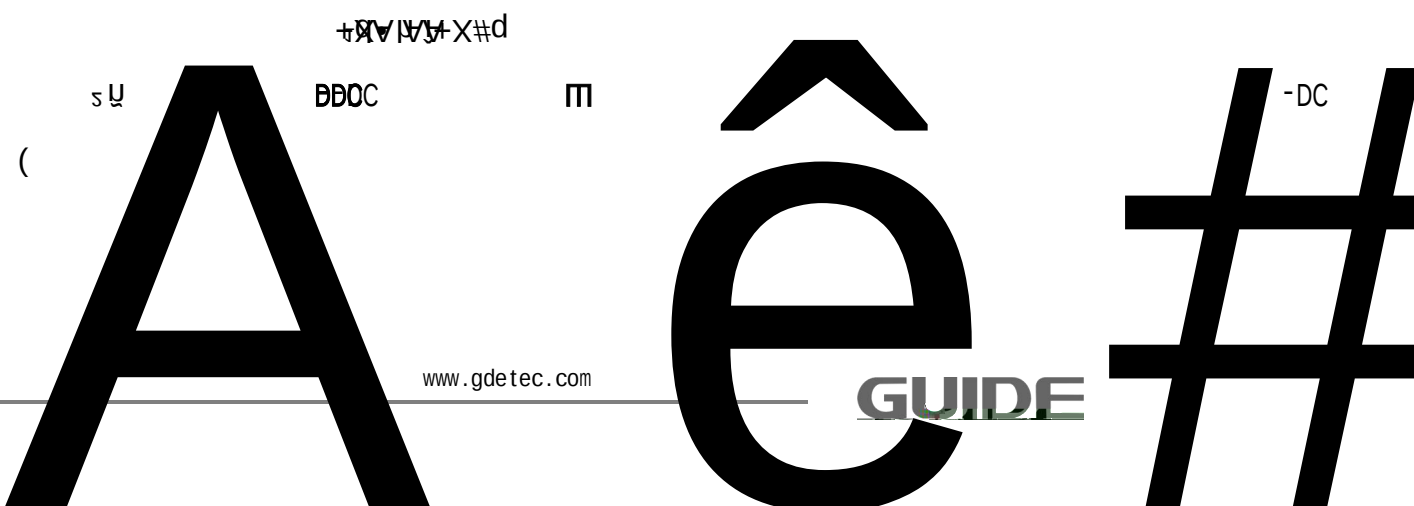
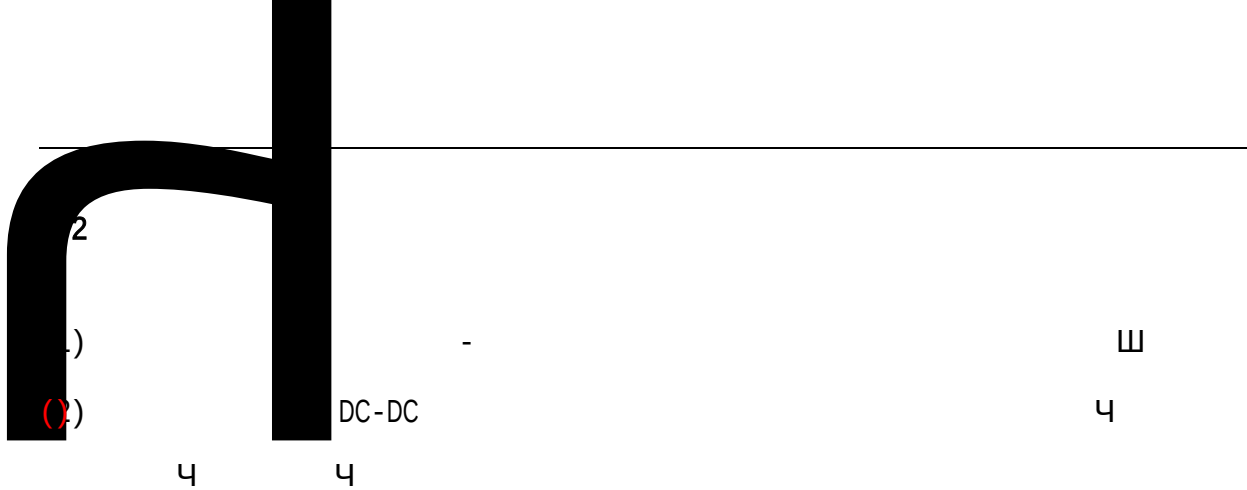
11.5

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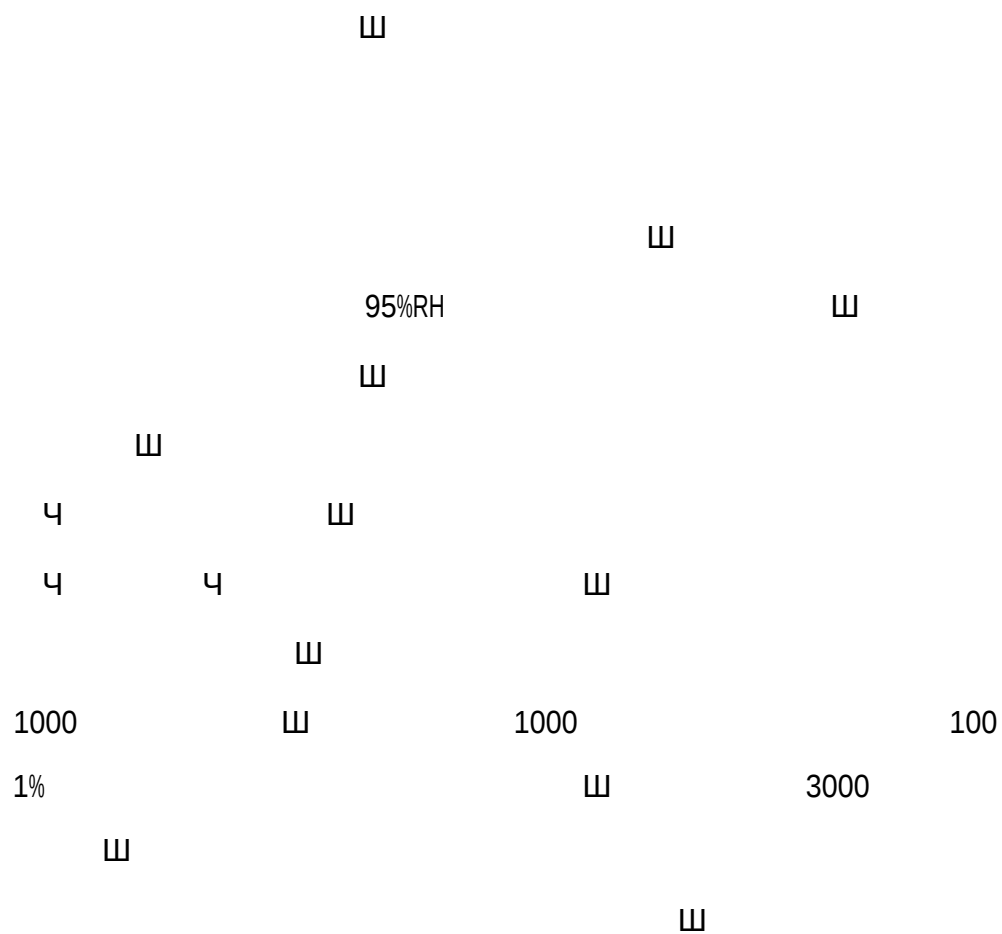




2.4 DC-DC

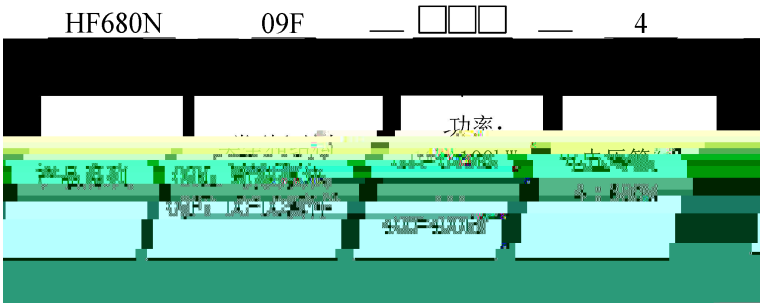
1. DC-DC / Ё Ё
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2.4.2



3.

3.1

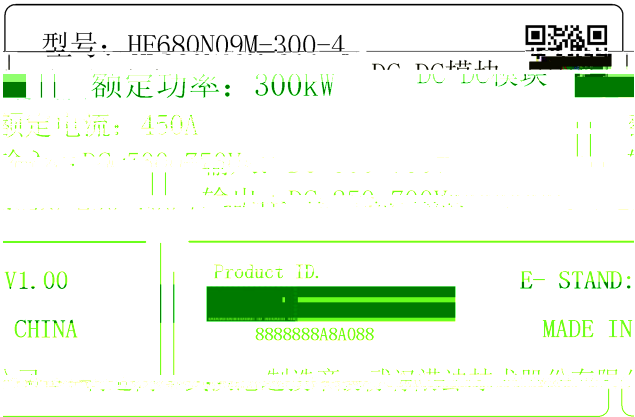


3.1.1

A	680N
B	09M DC-DC 09F DC-DC
C	3
D	400V

3-1

3.1.2



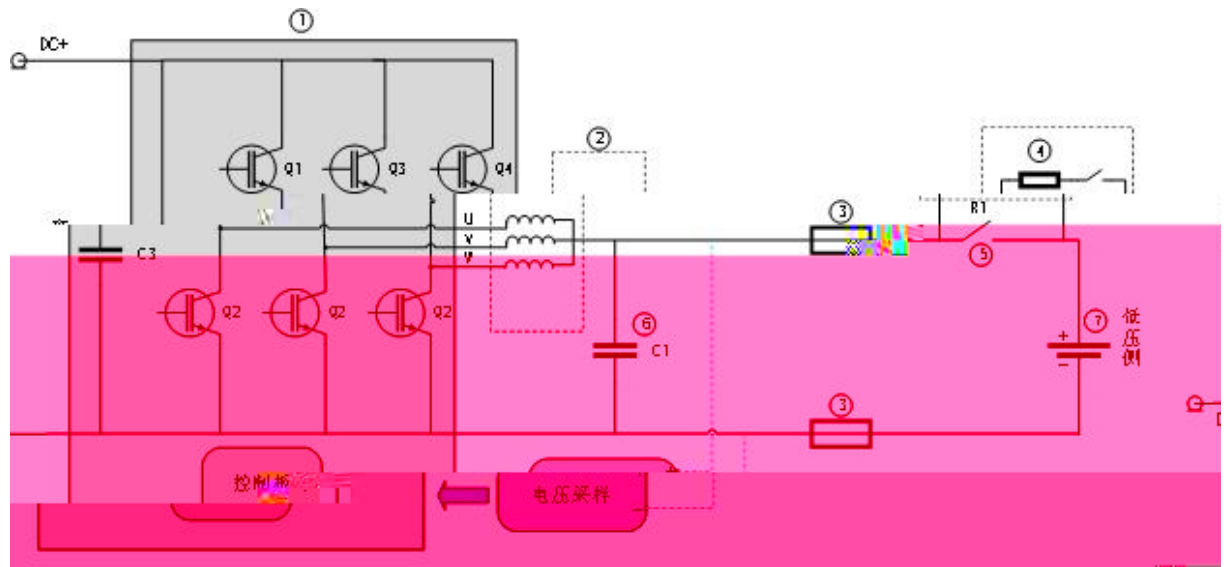
3.1.3 DC-DC

DC-DC		V _{DC} V	(A)	(kW)	V _{DC} (V)
HF680N09F-100-4	HF680N09M-100-4	250-700	150	100	500-750
HF680N09F-200-4	HF680N09M-200-4	250-700	300	200	500-750
HF680N09F-265-4	HF680N09M-265-4	250-700	400	265	500-750
HF680N09F-300-4	HF680N09M-300-4	250-700	450	300	500-750
HF680N09F-400-4	HF680N09M-400-4	250-700	600	400	500-750

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3.3.2

HF680N09F DC-DC



1-2 HF680N09F DC-DC

1	HF680N09M	1		
2		3		
3		-		
4		1		
5		-		
6		-		
7		-		4

DC-DC

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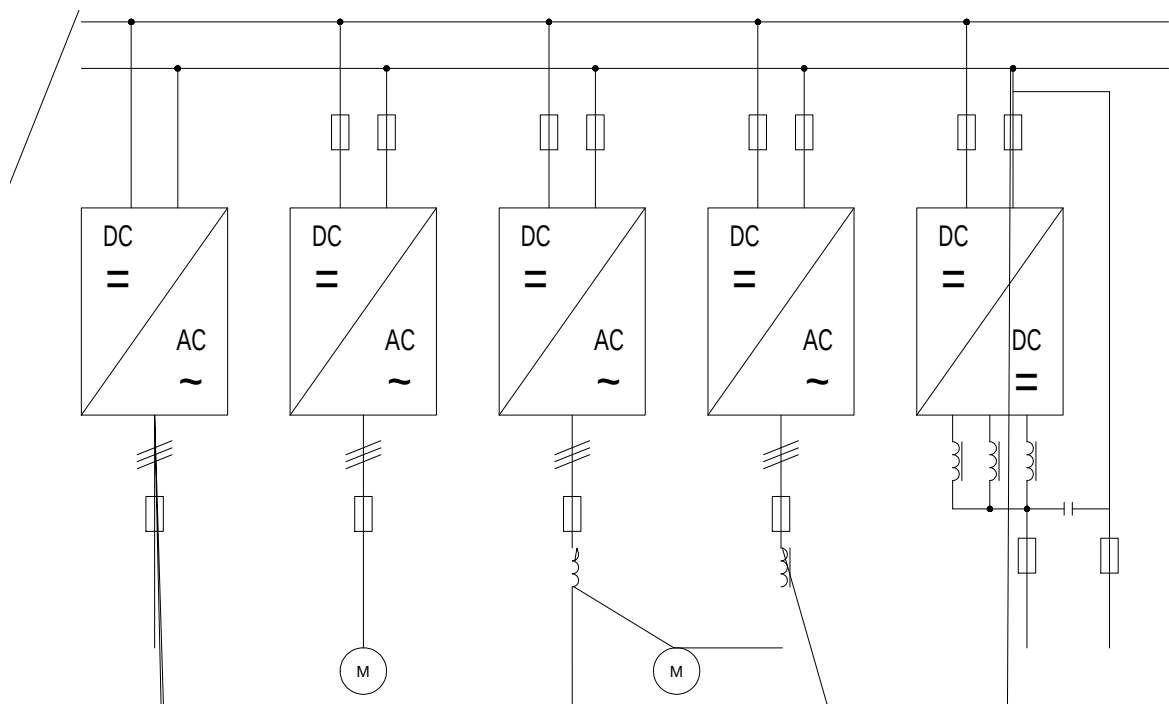
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3.3.3

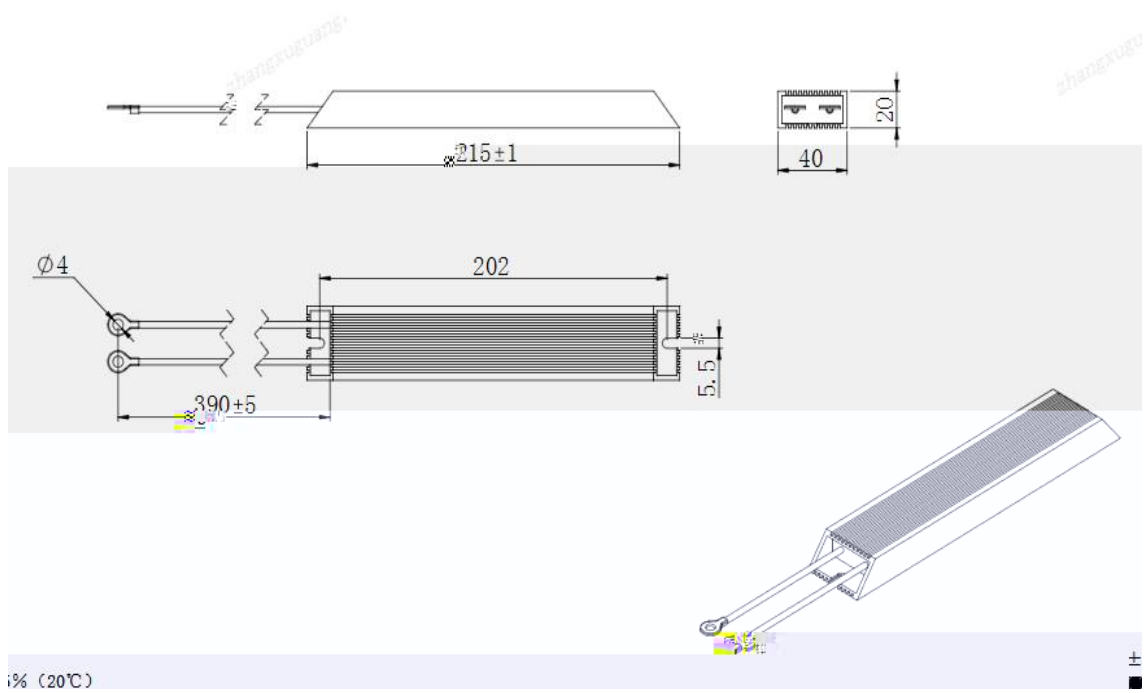


3.4

4.2.1

DC-DC

3.4.4



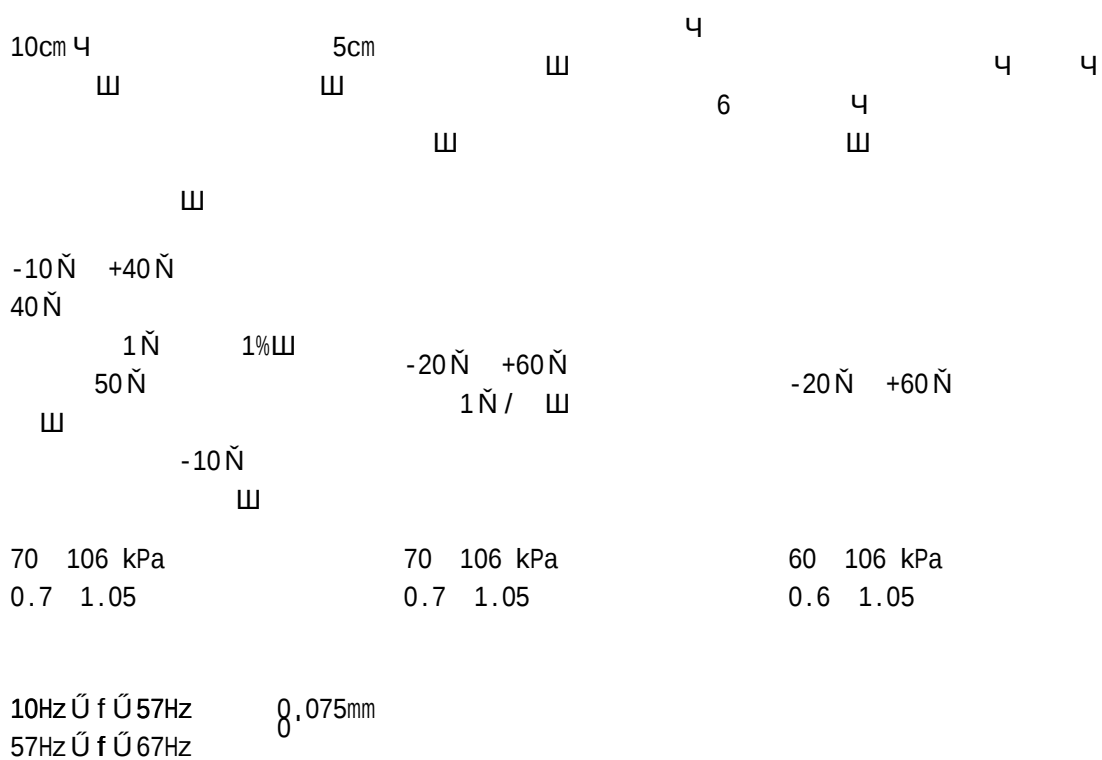
DC-DC

HF680N09F-100-4

GDHF680NHC-100-800

4.

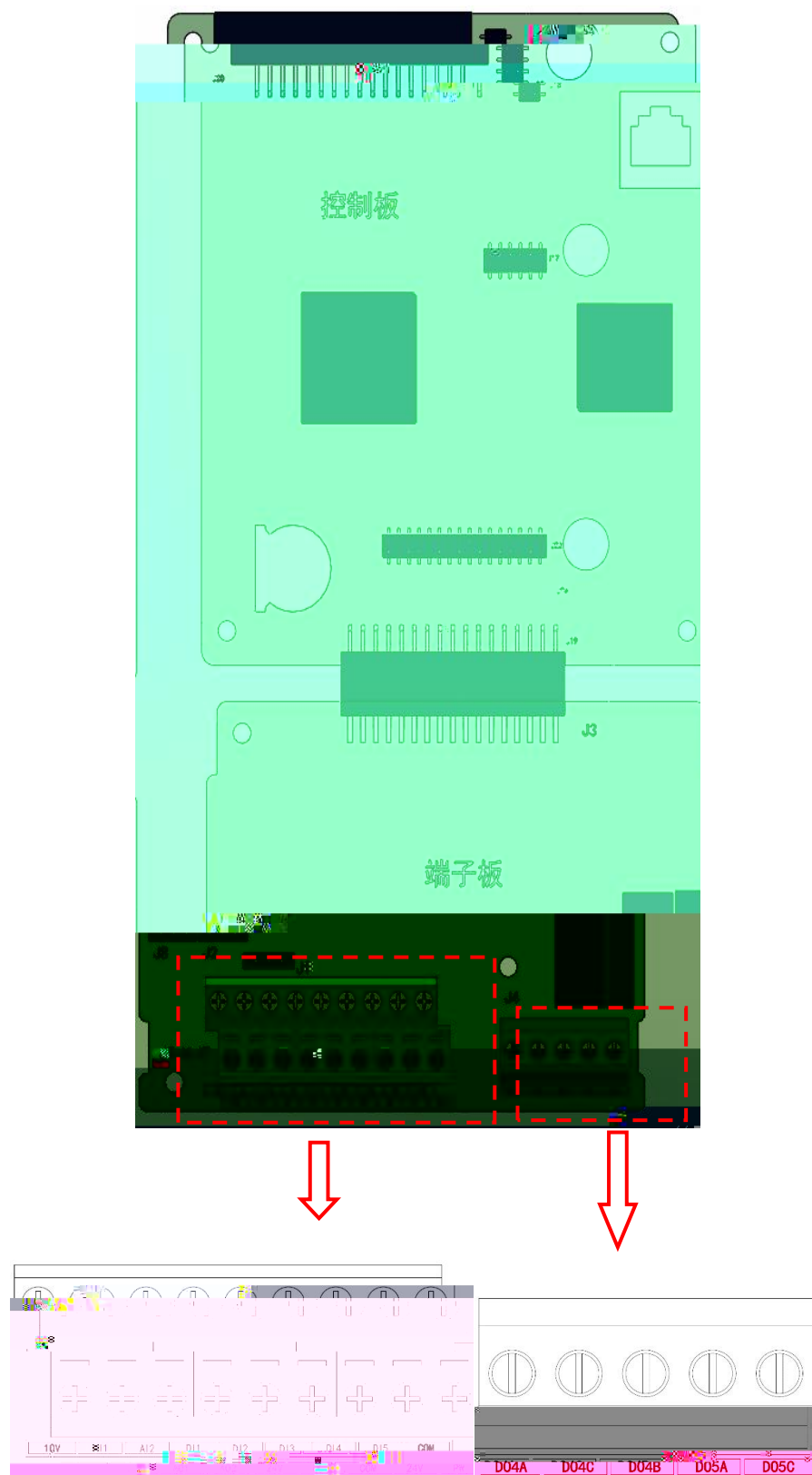
4.1 DC-DC Ч

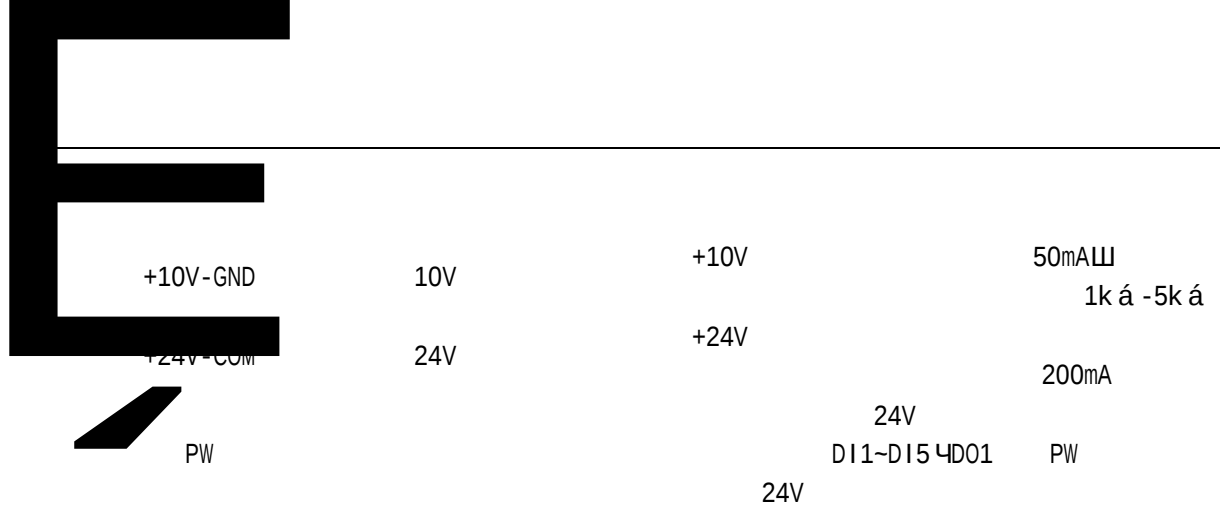


4.2.2

4.2.4

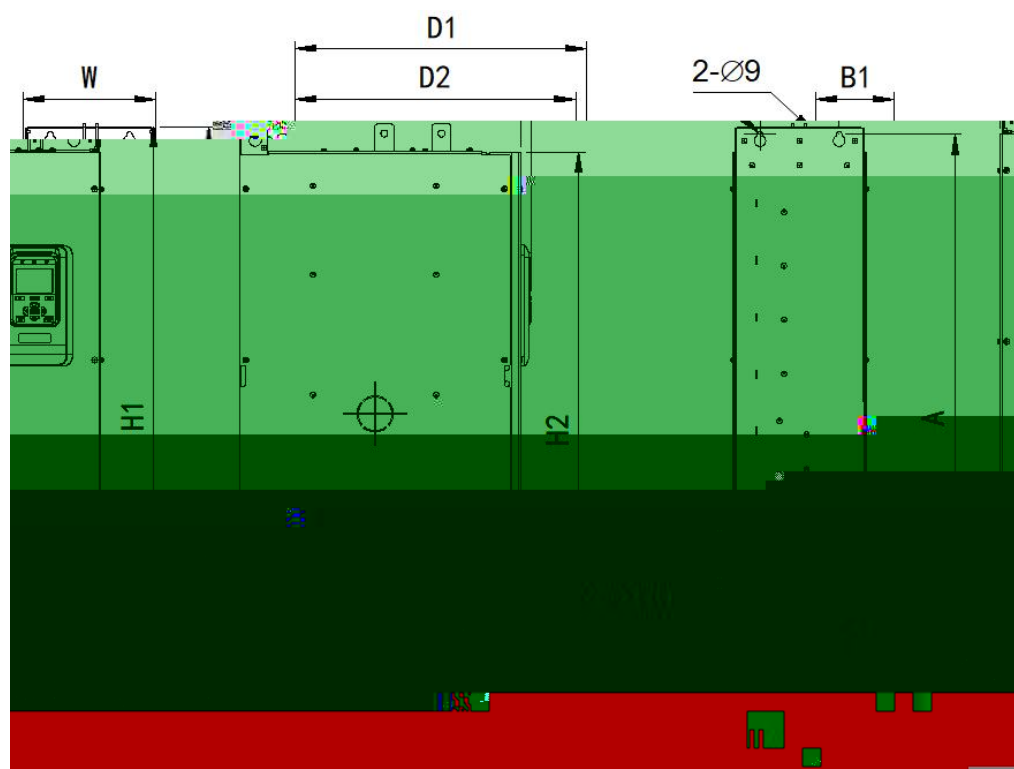
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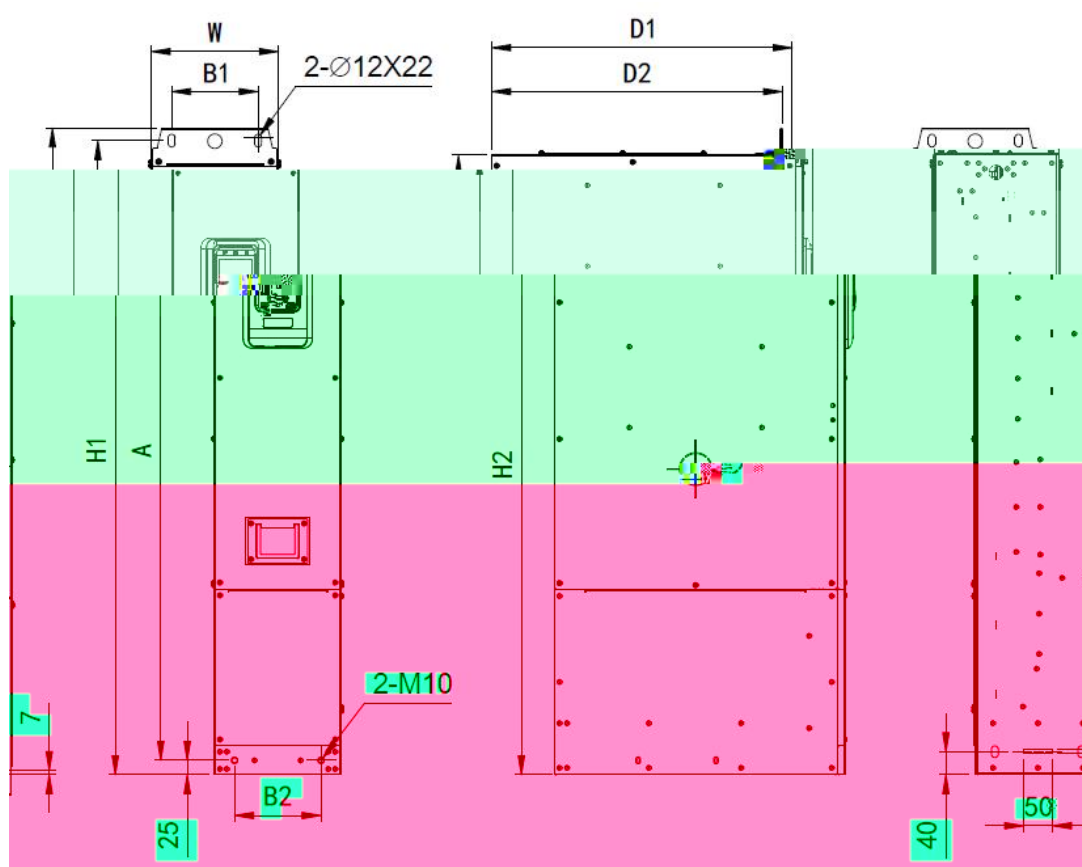


DC+24V

4.3



B4



B5

III

		mm					mm			8.8	kg
		H1	H2	W	D1	D2	A	B1	B2		
HF680N09M-100-4	B4	920	880	210	462	444	899	125	150	4-M8	55
HF680N09M-200-4											
HF680N09M-265-4											
HF680N09M-300-4											
HF680N09M-400-4	B5	1122	1075	221	522	505	1075	150	150	4-M10	80

5.

5.1

HF680N09M	DC-DC	山	F1	4
LOCAL/REMOTE	4 F2	4 RUN	4 STOP	4
			4 /RESET	4
	DC-DC		ENTER	山



W

W

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ENTER

W

RUN

STOP

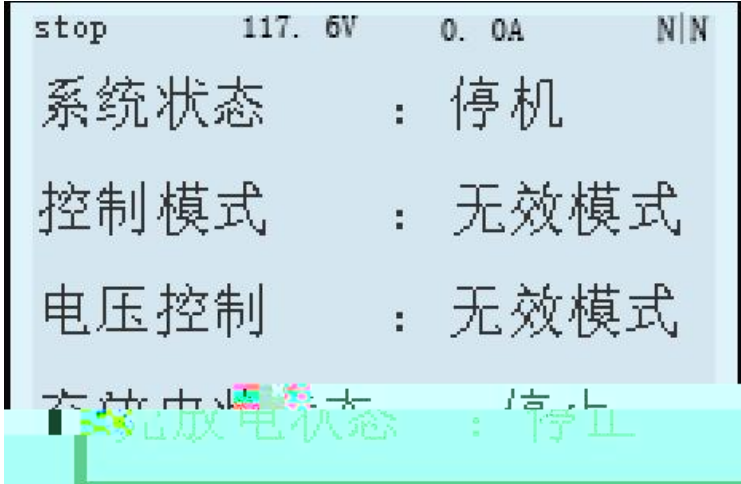
LOCAL/REMOTE

/

W

DCDC

W

[illegible]

stop	4 charge	4
Discharge		

$$:V$$

A

1

4

ENTER

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F1/F2

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1 Reference Set

2 Parameter Edit

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3 Current Error

4 Current Warning

5 Error Event Log

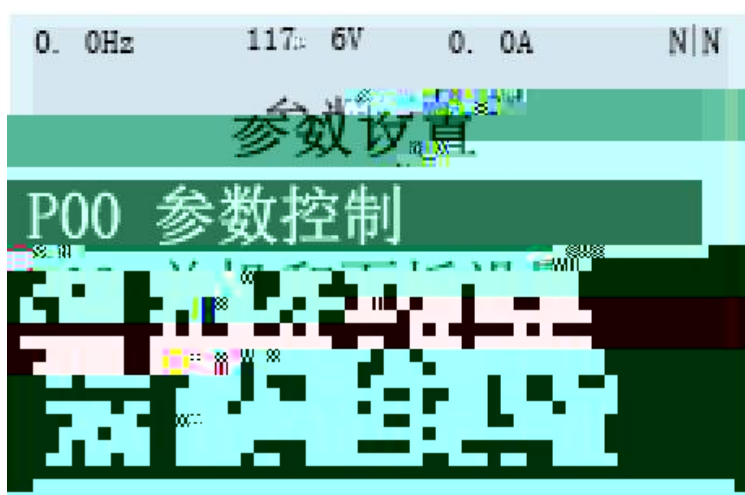
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7 Option Set

8

5	Version	
6	Disp Work Time	

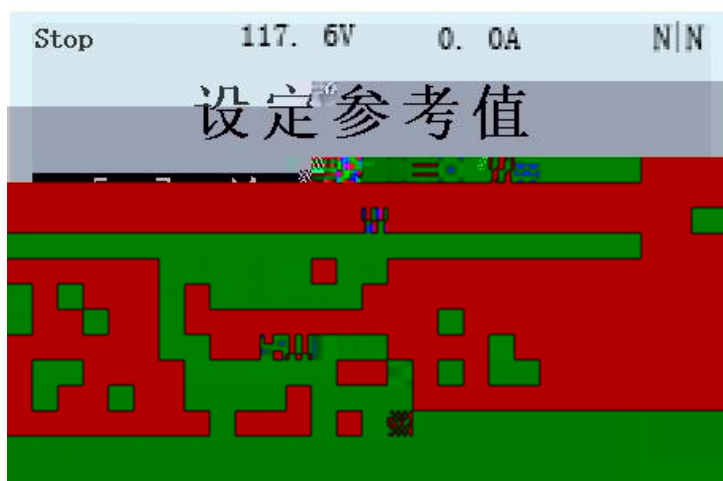
5.4.2③ IO



Parameter Setting

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5.4.3③ IO



Reference Set





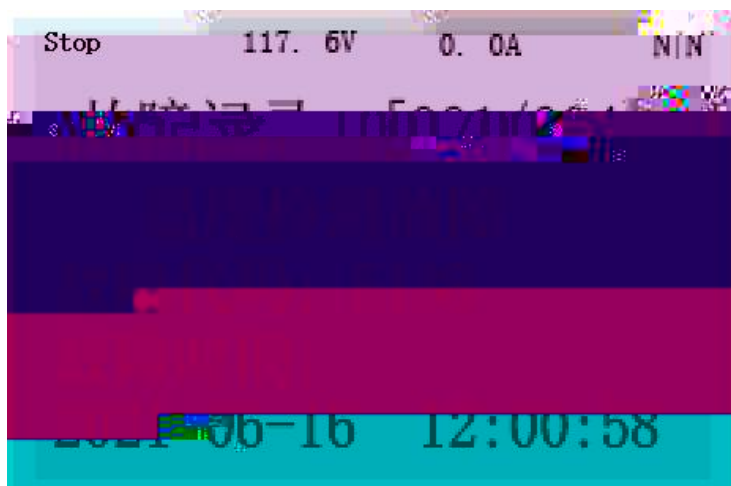
1	Parameter Initialization	
2	Delete Fault	
3	System Restart	
4	Backup Para Pad	
5	Restore Para Pad DSP	
6	Compare Para Pad	
7	Admin Password	

Enter

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5.4.5

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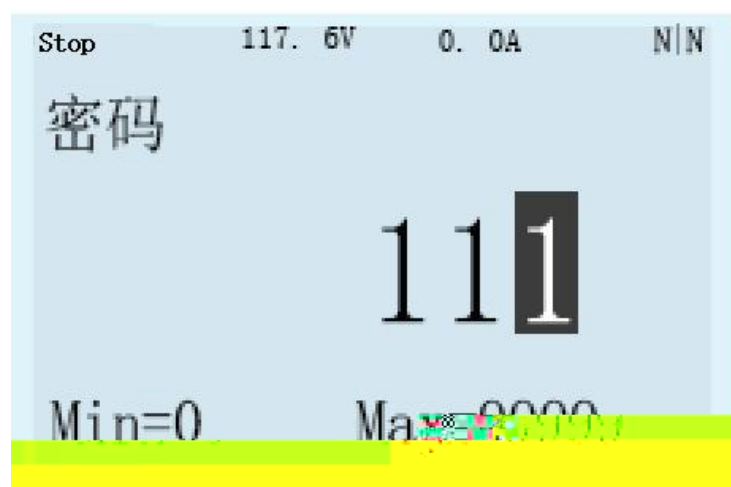
Fault Record

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5.4.6

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Access Permissions

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6.1.

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6.1.1

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P9.03 г д [1] P9.04 г д [1] ,
DCDC ш ,

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P9.03 г д [1] P9.04 г д [2] ,
DCDC ,

8 [└](#) [└](#) ш

P9.03 г д 1 г д P9.04 г д 3 г ш
д, DCDC ,

8 [└](#) [└](#) ш

6.1.2

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8.2.2

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P09.40 г

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P09.40

DCDC

P09.39 г

Д
P09.39

DCDC

6.123

DCDC

P9.03 г

Д 3

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. U2

$$K = \frac{1}{2}$$

$$P01.0 = K$$

6.2.2

. U1

. U2

$$K = \frac{1}{2}$$

$$P01.22 = K$$

6.2.3

A . Ia1

A . Ia2

$$K_a = \frac{1}{2}$$

$$P01.2 = K_a$$

B . Ib1

B . Ib2

$$K_b = \frac{1}{2}$$

$$P01.3 = K_b$$

B . Ic1

B . Ic2

$$K_c = \frac{1}{2}$$

$$P01.4 = Kc$$

6.3.

DCDC

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DCDC

6.3.1



P7.00

400V

[800V]

P7.01

6.3.6 ()

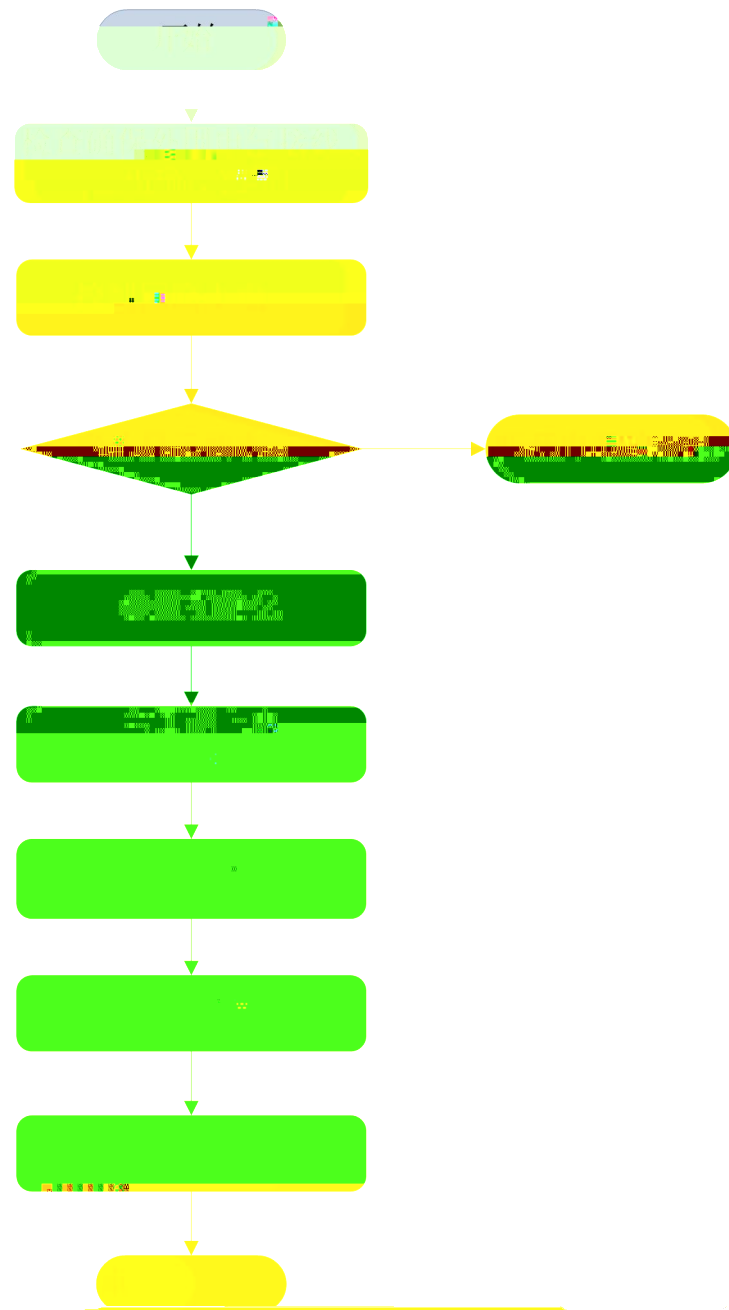
P7.18	()	() [2.0s]

6.3.7

P7.21		[0] [1]
P7.23		[0] [1]
P7.24		[0] [1]
P7.25		[0] [1]
P7.26		[0] [1]
P7.27		[0] [1]
P7.28		[0] [1]
P7.29		[0] [1]

7.

4



7.1.

7.2.

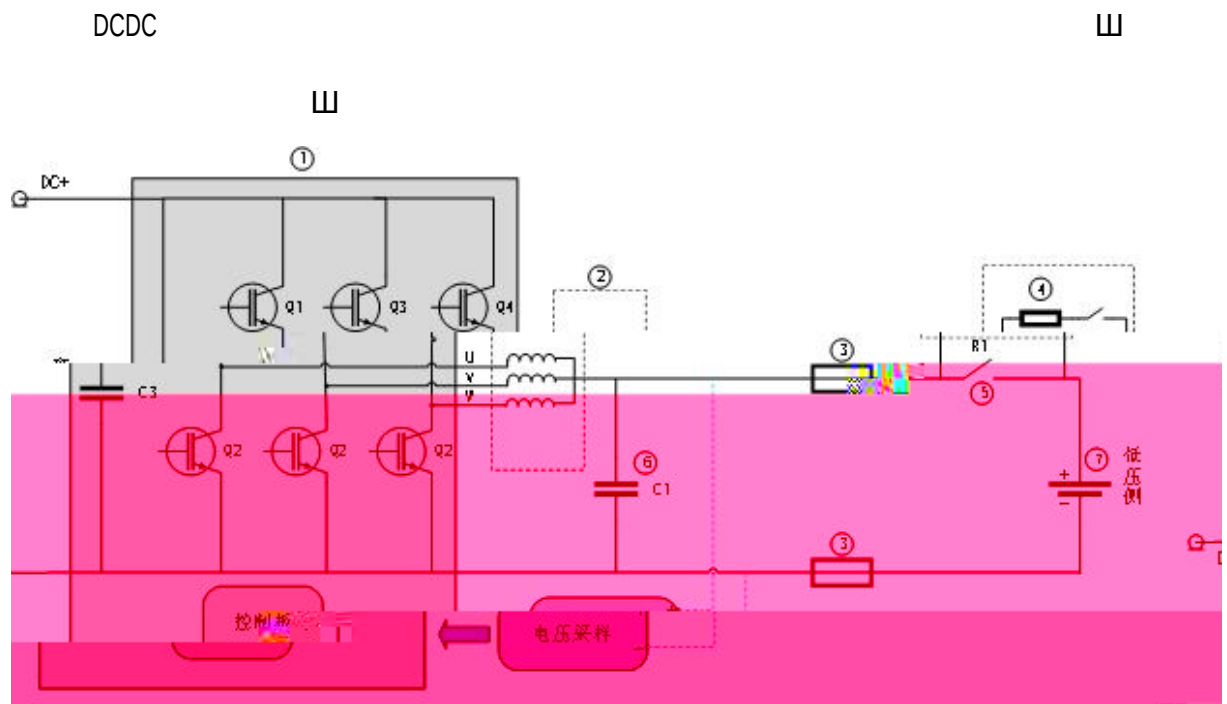
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7.3.2



7.4.

7.4.1

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P09.03 г

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P09.04

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P09.03

P09.04

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P09.51 г

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P09.51

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P09.21 г Dc-Link

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P09.52 г

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P09.52

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P09.26 г Dc-Link

д

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P09.26

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P8.04 г

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P8.04

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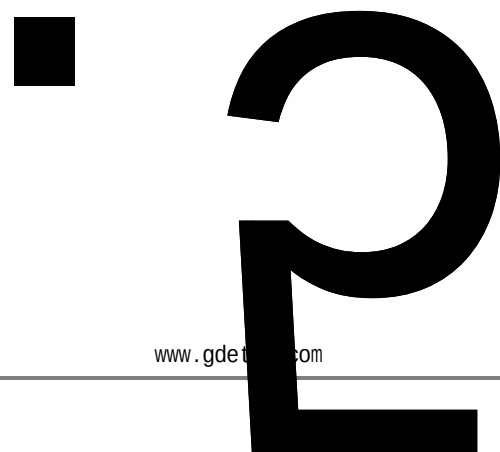
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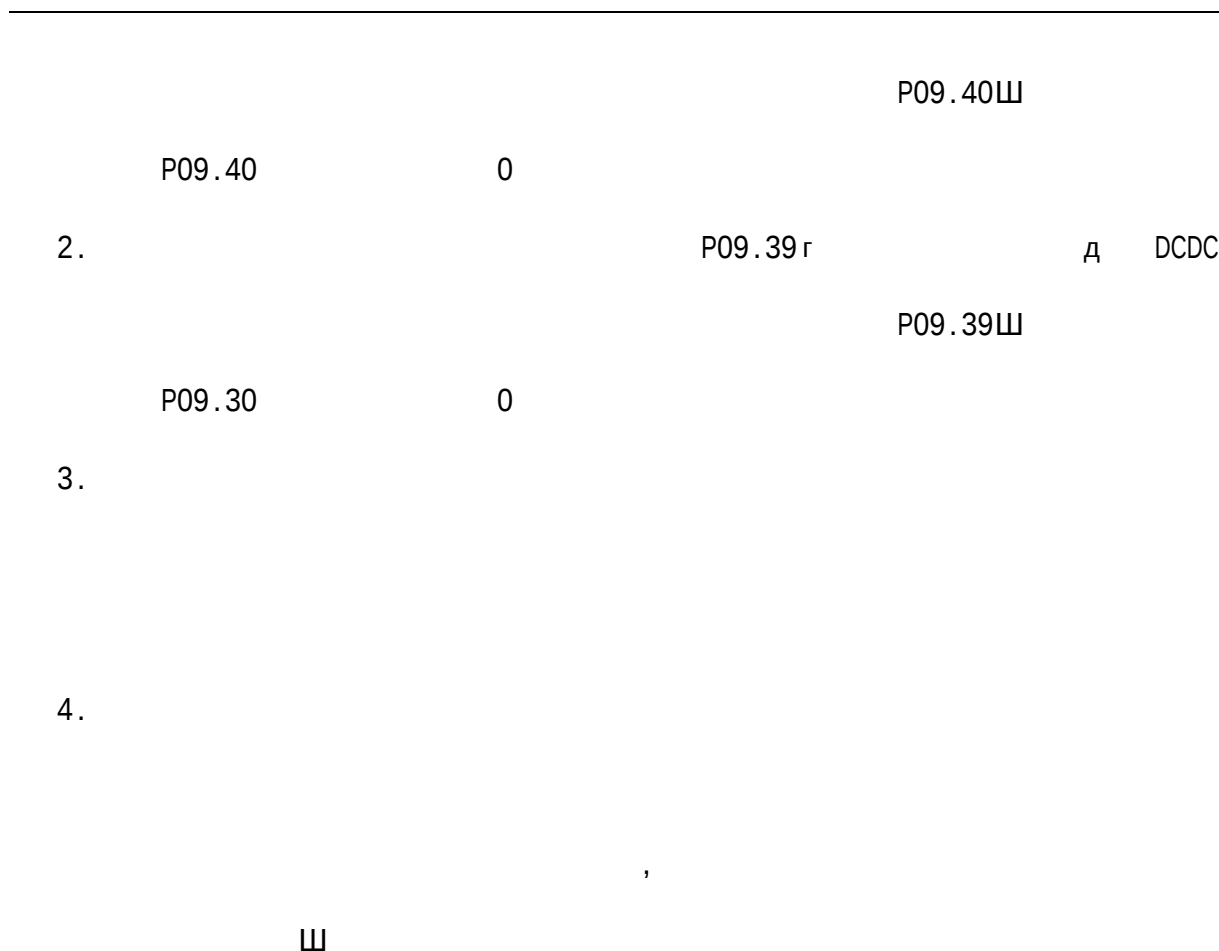
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7.4.4

P3.03	4	10 []	
P3.04	5	9 []	
P4.03	4	8 []	
P4.04	5	7 []	
P8.16		1.0	-
P8.17		2.5	-
P7.17		5.0	-
P7.18		2.0	-

		]	
P9.03		1 []	-
P9.04		1 []	-
P33.0	ProfiNet	0 1	1
P33.21	[W4]	1 [0]	
P33.23	[W5]	18 [V]	

	1	0	
Bit0			
Bit1			-
Bit2			-
Bit3			-
Bit4			
Bit5			
Bit6	.A		.A
Bit7	.B		.B
Bit8	.A		.A
Bit9	.B		.B
Bit10			-
Bit11			-
Bit12			-
Bit13			-
Bit14			-
Bit15			-

	1	0	
Bit0			-
Bit1			-
Bit2			-
Bit3			-
Bit4-5			00' 01' 10'
Bit6-7			00' 01' 10' 11'
Bit8-9			00' 01' 10' 11'
Bit10			-
Bit11			-
Bit12			1 0

Bit13-15			000'	
			001'	
			010'	
			011'	
			100'	
			101'	
			110'	
			111'	PB

[W4] bit0 =1 DCDC bit0=0 DCDC
[W5] 0-1000 0 1000V

7.5.4 Modbus

Modbus

P8.00		3 [MODBUS]	-
P8.01		1 []	-
P8.02		1 []	-
P8.03		1 []	-
P8.04		3 [Modbus]	-
P9.03		1 []	-
P9.04		1 []	-

Modbus

	1	0	
Bit0			
Bit1			-
Bit2	LOCAL	REMOTE	
Bit3	FAULT_RESET		-
Bit4	EXT_FAULT		
Bit5	EXT_ALARM		
Bit6	CtrlMode.A		.A
Bit7	CtrlMode.B		.B
Bit8	VoltCtrlType.A		.A
Bit-			

Bit0	Ready	
Bit1	RUN	Sto

1 01 Address = 1 01

8.

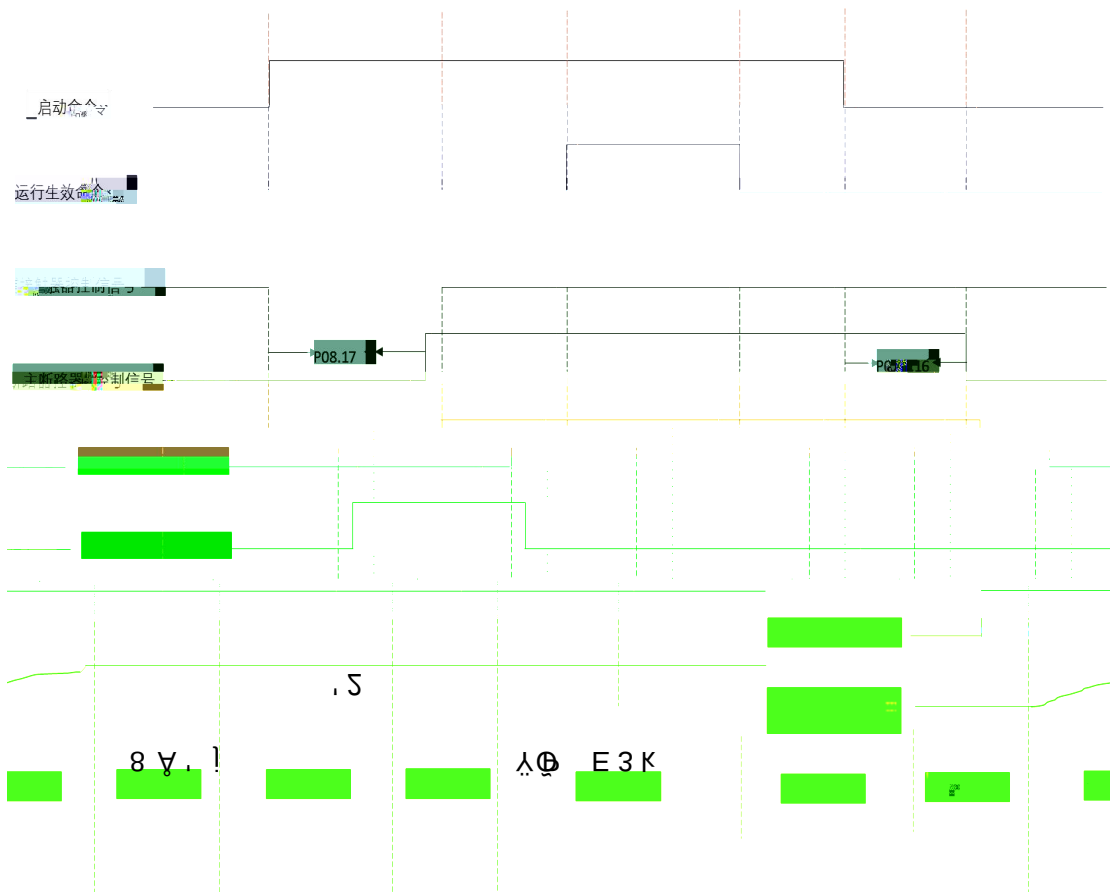
8.1.

III

III

8.1.1

I



1> P7.17 5 P7.18
2 2 /
2> P7.17 P7.18
2 /
3> D04 DI4
4> D05 DI5
5> c

Bit6	.A		.A
Bit7	.B		.B
Bit8	.A		.A
Bit9	.B		.B
Bit10			-
Bit11			-
Bit12			-
Bit13			-
Bit14			-
Bit15			-

8.1.3

	1	0	
Bit0			-
Bit1			-
Bit2			-
Bit3			-
Bit4-5			00'
			01'
			10'
Bit6-7			00'
			01'
			10'
			11'
Bit8-9			00'
			01'
			10'
			11'
Bit10			-
Bit11			-
Bit12			m "

8.1.4

bit13-15		
000(S1)		S2
001(S2)		S3
101(S3)		S4.
110(S4)		S6
010(S5)		S5
011(S6)		S2
100(S7)		S6 S7
111(S8)	PB	PN S2 S8

8.2.

8.2.1

P10.00

W

P8.01		[0] IO [1] [2] Modbus [3] Dp
P8.02		[0] IO [1] [2] Modbus [3] Dp
P9.03		[0] [1] [2] [3]
P9.04		[0] [1] [2] [3]
P9.00		

		()
P8.04		[0] [1]AI1 [2]AI2 [3]Modbus [4]Dp
P8.08		5.0%/s, ;

P8.04

0

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5.4.3Э ЮЛ

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P8.01		[0] IO [1] [2]Modbus [3]Dp
P8.02		[0] IO [1] [2]Modbus [3]Dp
P9.03		[0] [1] [2] [3]
P9.04		[0] [1] [2] [3]
P9.00		()
P8.08		5.0%/s, ;
P9.21	Dc-Link	
P9.26	Dc-Link	
P9.51		0: 1 Modbus 2 Dp
P9.52		0: 1 Modbus 2 Dp

		[1] [2] ! W [3]
P9.01		,
P8.04		[0] [1]AI1 [2]AI2 [3]Modbus [4]Dp
P8.06		0: P8.04 1:
P8.09		; ; 5.0%/s,

P8.04 0.01 (' W .. ° W
1.5.4.3.3 Ю Ł ; І % Ł
І А Ł ;
P8.06 0 P8.04 1
1.8.3.1 Ł

8.2.3



P8.01

..

В]W B

1]Wopus

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' W ..

В 5

55

		[4]Dp
P8.09		; 5.0%/s,

P8.04 0 W

[15.4.39](#) [10 L](#) ;

P8.12 =2
P8.12 =3

80%

8.3.

8.3.1

DCDC

DCDC

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8.4.1 Modbus

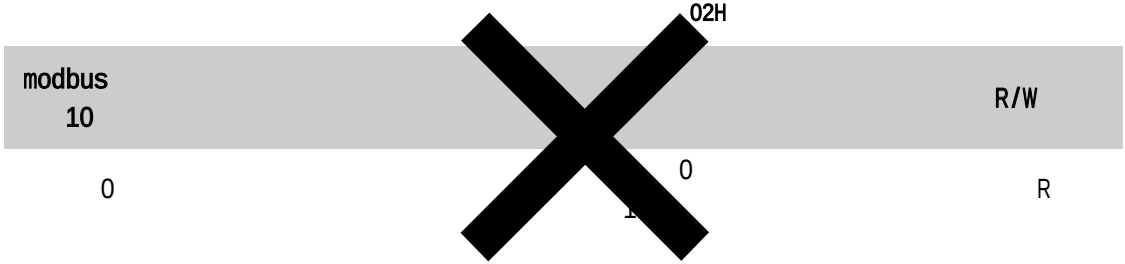
Modbus

P32.01	MODBUS ID	Min=1 Max=255 1
P32.02	MODBUS	[0]:RS485; [1]:RS232 RS485
P32.03	MODBUS	[0] 9600 BPS [1] 14400 BPS [2] 19200 BPS [3] 38400 BPS [4] 56000 BPS [5] 57600 BPS [6] 115200 BPS
P32.4		[0] None_8_1_CFG [1] Even_8_1_CFG [2] Odd_8_1_CFG [3] None_8_2_CFG [4] Even_8_2_CFG [5] Odd_8_2_CFG
US	W	MODB

Modbus

05H			
modbus 10			R/W
0		0: 1	W
1			-
2		1	W
3		1	W
4		0 1	W
5		0 1	W
6	.A	00: 01:	W
7	.B	10:	W

		11:	
8	.A	00:	W
9	.B	01:	
		10:	W
		11:	
10-15			-



7

-

0X 1 1

-

modbus 10				R/W
0	DI	DI		R
50	1[V]	100 => 1.0[V], -10 ~ +10[V]		R
51	1[I]	100 => 1[mA], 0 ~ 20[mA]		R
52	2[V]	100 => 1.0[V], -10 ~ +10[V]		R
53	2[I]			

12	B		100=>10.0A	R
13	C		100=>10.0A	R
17			100=>10.0V	R
18			100=>10.0V	R
19			100=>10.0	R
21		%	100=>1.0%	R
22		%	100=>1.0%	R
23		%	100=>1.0%	R
50	A01	1	10 => 1.0[%]	R/W
51	A02	2	10 => 1.0[%]	R/W
60			[07..00] : [15..08] :	R
62			[07..00] : [15..08] :	R
100		[0.1%]	10 => 1.0%	R/W
101		0.1V]	10 =>1.0V	R/W
102		0.1%	10 =>1.0%	R/W
103		0.1A	10 =>1.0A	R/W
104		0.1%		R/W
105				

8.4.2 PN

DCDC PN PN . PN ,
PN DCDC 山 山

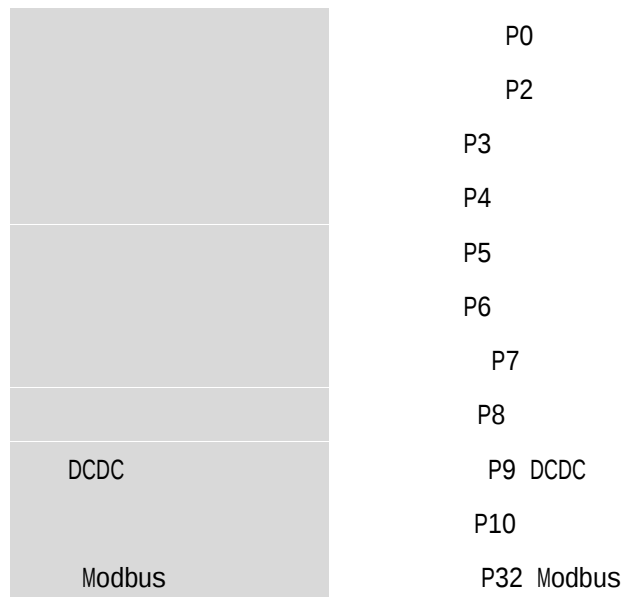
	1	0
Bit0		
Bit1		-
Bit2		-
Bit3		-
Bit4		
Bit5		
Bit6	.A	00'

n n

			101'
			110'
			111' PB

9.

9.1.



1		
3	.a	1 0 00
4	.b	01 10 11
5	.a	1 0 00
6	.b	01 10 11
7		
8		
9		
10		
11		
12	.NC	
13	FUNC 12	
14	FUNC 13	
15	FUNC 14	
16	FUNC 15	

9.2.2

P4

P4.0	1		0 15	0
P4.1	2		0 15	0
P4.2	3		0 15	0
P4.3	4		0 15	0
P4.4	5		0 15	0

0		
1		
2		ON
3		

P5.6	AI1	AI1	-300.0 300.0 [%]	0.0 [%]
P5.7	AI1	AI1	-10.00 10.00 [V]	10.000 [V]
P5.8	AI1	AI1	0.00 20.00 [mA]	20.000 [mA]
P5.9	AI1	AI1	-300.0 300.0 [%]	100.0 [%]
P5.18	AI2	[0] [1]0 +10V [2]-10 +10V [3]0 20mA	0 3	3
P5.19	AI2	AI2 Ш Ш Ш	0.0 1000.0 [ms]	25.0 [ms]
P5.20	AI2	AI2	-10.00 10.00 [V]	0.000 [V]
P5.21	AI2	AI2	-20.00 20.00 [mA]	0.000 [mA]
P5.22	AI2	AI2	-10.00 10.00 [V]	0.000 [V]
P5.23	AI2	AI2	0.00 20.00 [mA]	0.000 [mA]
P5.24	AI2	AI2	-300.0 300.0 [%]	0.0 [%]
P5.25	AI2	AI2	-10.00 10.00 [V]	10.000 [V]
P5.26	AI2	AI2	0.00 20.00 [mA]	20.000 [mA]
P5.27	AI2	AI2	-300.0 300.0 [%]	100.0 [%]

9.2.4 ð q

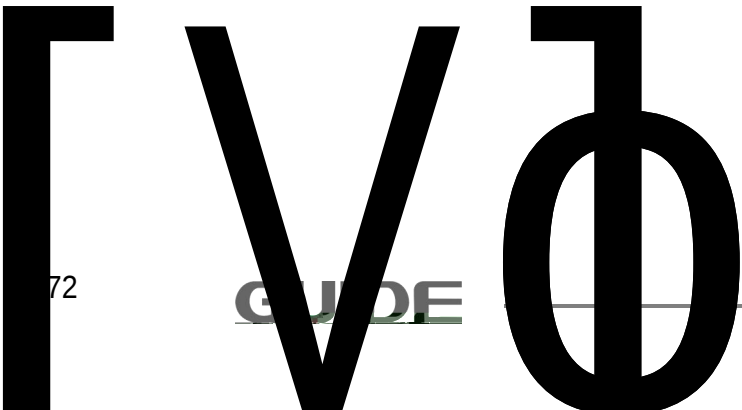
P6.21	A02	A02 (P6.14 [13])	0.0 100.0 [%]	0.0 [%]
P6.22	A02	A01 Ш Ш Ш	0.0 1000.0 [ms]	10.0 [ms]

7-1

0		
1		
2		
3		()
4		(150Ñ)
5	DP	Profibus
6		P6.7 P6.21
7		

9.2.5 P7

P7.00	600 820 800 [V]
P7.01	300 500 350 [V]
P7.02	:0 "



9.2.6

P8

P8.00		[0] [1] [2]Dp [3]MODBUS [4]	0 4	1
P8.01		[0]IO [1] [2]Modbus [3]Dp	0 3	1
P8.02		[0]IO [1] [2]Modbus [3]Dp	0 3	1
P8.03		[0] () [1]	0 1	1
P8.04		[0] [1]AI1 [2]AI2 [3]Modbus [4]Dp	0 4	0
P8.06		0: P8.04 1:	0 1	0
P8.08			0.0 300.0 [%/s]	5.0 [%/s]
P8.09			0.0 300.0 [%/s]	5.0 [%/s]
P8.11		[0] [1]	0 1	1
P8.12		[0] [1] [2] [3] 80%	0 3	3
P8.13		P8.12=1 P8.13	0 300 [%]	90 [%]
P8.14			0.01 5.000 [s]	0.1 [s]
P8.15			0.01 5.00 [s]	0.5 [s]
P8.16			0.1 6000.0 [s]	1 [s]
P8.17			0.1 10.0 [s]	2.5 [s]

		1: Even_8_1_CFG 2: Odd_8_1_CFG 3: None_8_2_CFG 4: Even_8_2_CFG 5: Odd_8_2_CFG		
P32.05			0, 100 [s]	0 [s]

9.2.10

P33

P33.0	Profibus	[0]	0 1	0
		[1]		
P33.1		PLC	1 255	1
		[0]PPO 1		
P33.2		[1]PPO 2	0 3	2
		[2]PPO 5		
		[3]GUIDE		
P33.3			0 16	14
P33.4			0 16	14
		[0]		
P33.5		[1]		
		[2]		
		[3]		

			[4] § 10000			
P33.17	[W2]		7-2	0 38	0	
			[0] § 1	0	1	
			[1] § 10			
P33.18	[W2]		[2] § 100	0 4	0	
			[3] § 10000,			
			[4] § 100000	0	0	
P33.19	[W3]		7-2	0 38	0	
			[0] § 1			
P33.20	[W3]		[1] § 10			
			[2] § 100	0 4	0	
			[3] § 1000			
			[4] § 10000			
P33.21	[W4]		7-2	0 38	1	
			[0] § 1			
P33.22	[W4]		[1] § 10			
			[2] § 100	03.43	0	
			[3] § 1000			
			[4] § 10000			
P33.23	[W5]		7-2	0 38	18	
P33.13	[W6]		7-2			

[§§]

		[2] § 100 [3] § 1000 [4] § 10000		
P33.45	[W0]	7-3	0 48	0
P33.46	[W0]	[0] § 1 [1] § 10 [2] § 100 [3] § 1000 [4] § 10000 [5] [%] § 1 [6] [%] § 10 [7] [%] § 100	0 7	0
P33.47	[W1]	7-3	0 48	0
P33.48	[W1]	[0] § 1 [1] § 10 [2] § 100 [3] § 1000 [4] § 10000 [5] [%] § 1 [6] [%] § 10 [7] [%] § 100	0 7	0
P33.49	[W2]	7-3	0 48	0
P33.50	[W2]	[0] § 1 [1] § 10 [2] § 100 [3] § 1000 [4] § 10000 [5] [%] § 1 [6] [%] § 10 [7] [%] § 100	0 7	0
P33.51	[W3]	7-3	0 48	0
P33.52	[W3]	[0] § 1 [1] § 10 [2] § 100 [3] § 1000 [4] § 10000 [5] [%] § 1 [6] [%] § 10 [7] [%] § 100	0 7	0
P33.53	[W4]	7-3	0 48	1
P33.54		[0] § 1 [1] § 10	0 7	0

P33.63

[W9]

7-3

0 48

13

[0] § 1

P33.64

[W9]

22	[Kw]
23	[%]
24	[A]
25	[A]
26	[A]
27	1[%]
28	2[%]
29	[%]
30	[%]
31	[V]
32	[V]
33	[V]
34	[V]
35	1[V]
36	2[V]
37	1[V]
38	2[V]

7-3

0	
1	0
2	1
3	2
4	3
5	4
6	5
7	0 32bit
8	1 32bit
9	2 32bit
10	3 32bit
11	4 32bit
12	5 32bit
13-16	
17	
18	
19	
20	
21	A
22	
23	Kw
24	A
25	B
26	C
27	A

10.

10.1



W01 SYS_NOT_RDY (Ready)

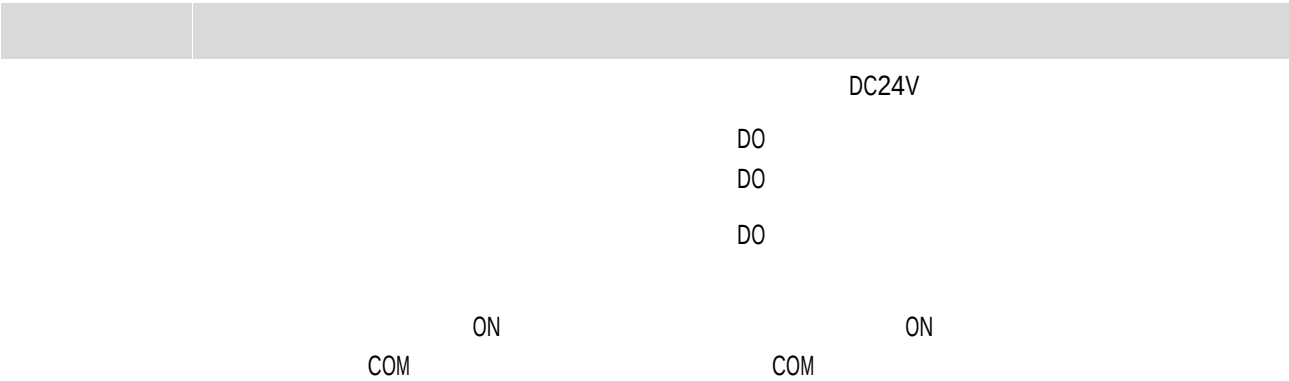
W25	5 SLV5_CAN_ERR	5	5

10.2

[E050]	U	ERR_UT not reset	IGBT	IGBT	
[E052]	V	ERR_UT not reset	IGBT	IGBT	
[E054]	W	ERR_UT not reset	IGBT	IGBT	
[E065]			DCDC	DO	DO
			DCDC	DI	
[E066]				DO	DO
			DCDC		

[E220]	CRC MEMORY CRC ERR	
[E221]	PARAMETER ERROR	

10.3



11.

1.

11.2

	1. Ч 2. Ч	1. > 40 Н < 95% 2. Ч
	1. 2.	1. 2.
	1. Ч 2. 3. Ч	1. Ч 2. Ч 3.
Ч	1. 2.	1. 2.

11.3

DC-DC

	Ч	
	Ч	
	Ч	
	2	1 2
PCB	Ч	

GUIDE

1 4	DC-DC	Ⅲ
2 4		Ⅲ
3 4		Ⅲ

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